



2815
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Akio KITAMURA

Serial No.: 10/055,722

Group Art Unit: 2815

Filed: January 23, 2002

Examiner: Fenty, J.

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND MANUFACTURE
METHOD THEREFORE

Certificate of Mailing

I hereby certify that this paper is being deposited with the
United States Postal Service as first class mail in an
envelope addressed to: Assistant Commissioner of Patents,
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By: Marc A. Rossi

Marc A. Rossi

#8
Election
J.W. Chinillo
11/8/02
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RESPONSE TO ELECTION / RESTRICTION

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Election / Restriction requirement dated October 8, 2002, applicant
wishes to elect Group I, claims 1 to 8 for prosecution.

Respectfully submitted,

Date: 11/01/02

Marc A. Rossi
Registration No. 31,923

Attorney Docket No. FUJI:203